

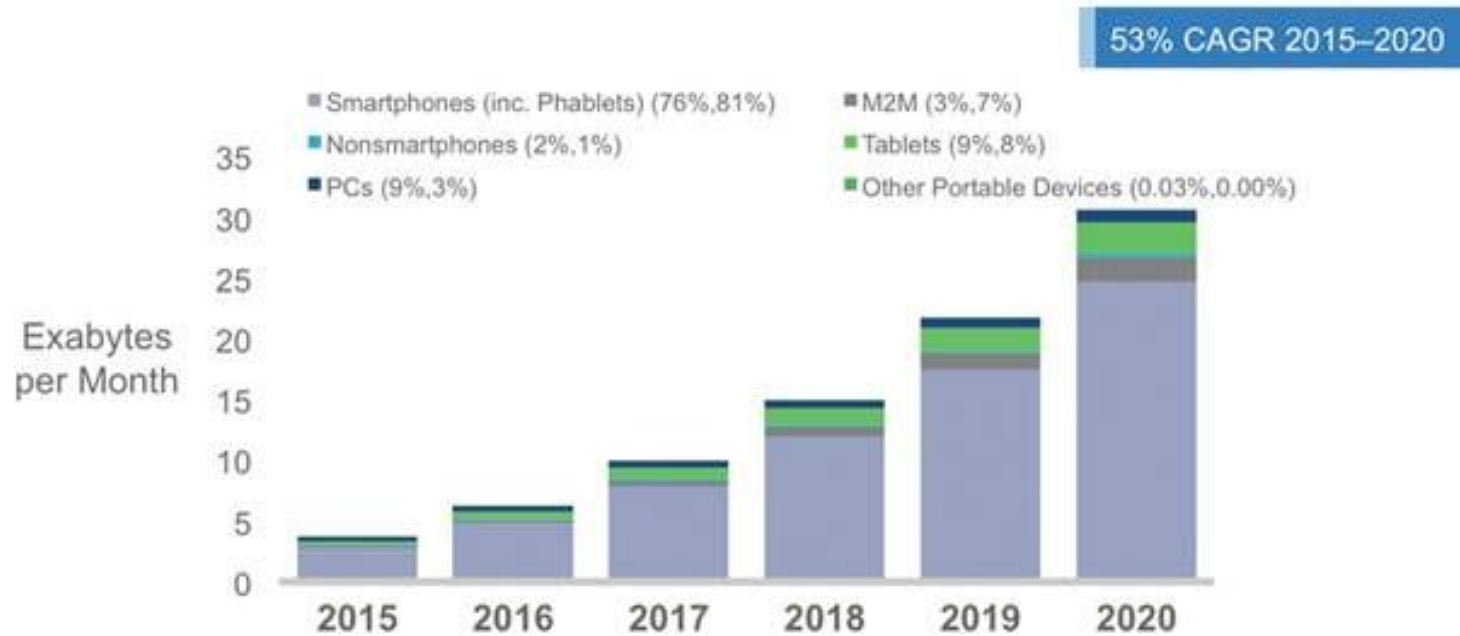
# P4-enabled NICs

## Acceleration of OVS

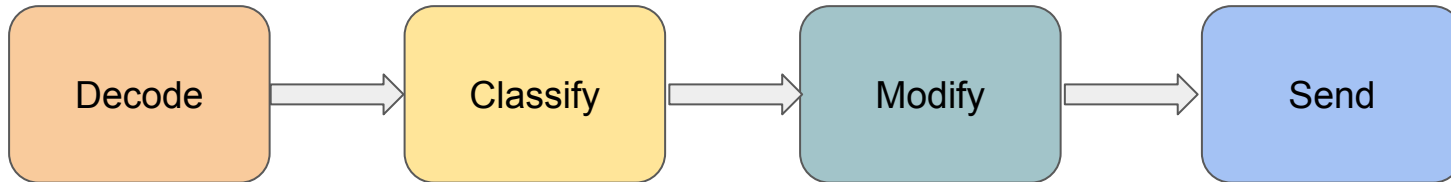


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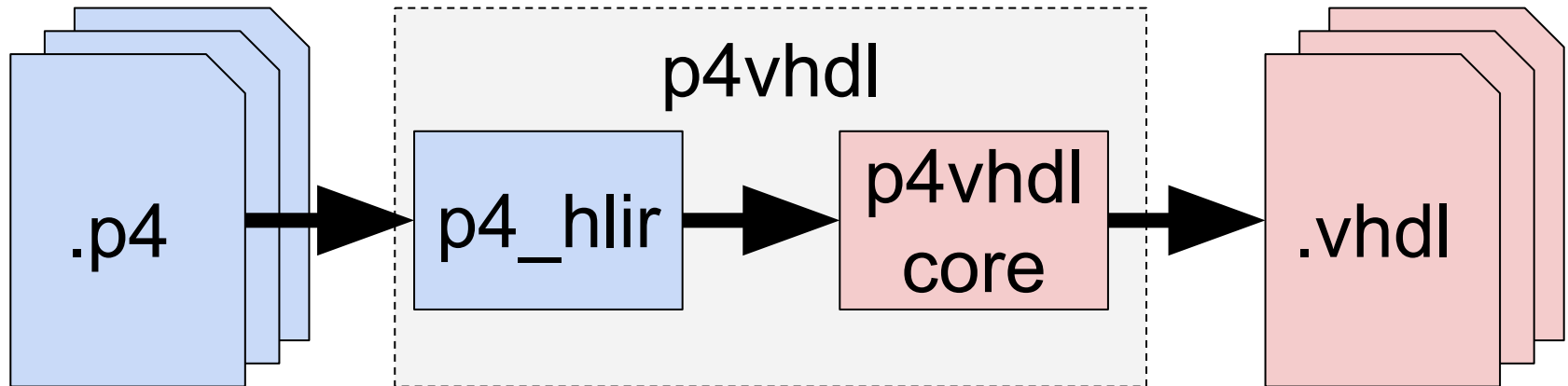
# Challenge - volume & complexity



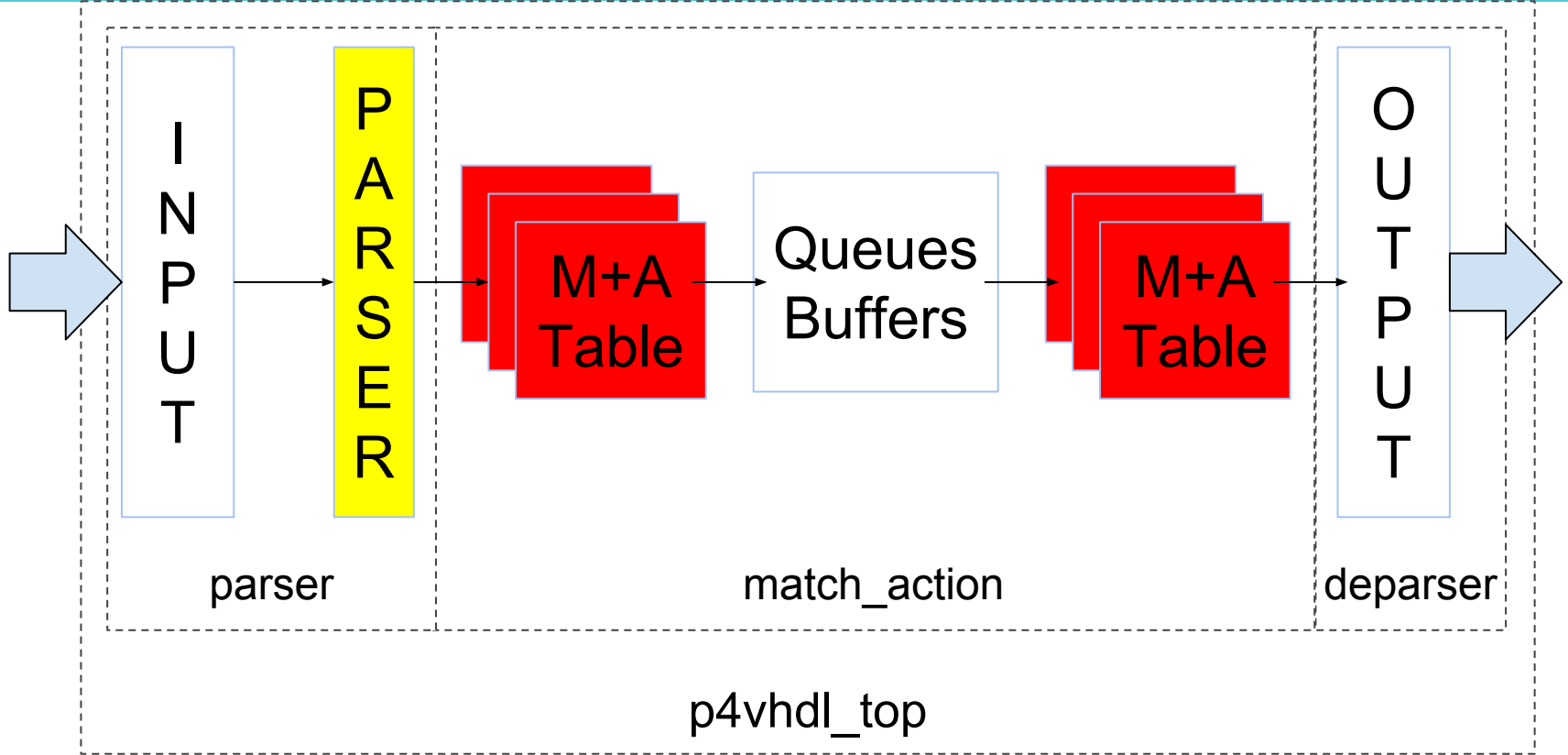
- Packet header processor
  - Configurable decoding of packets now running at 100G and 400G under preparation
- Lookups and classifiers
  - various hashes: cuckoo hash, perfect hash; bloom filter, TCAMs, Tries, pattern matching
- Packet editing IPs (e.g. encryption)
- Network interfaces 10/40/100GE
- Software interfaces DPDK and NDP API



- Virtualization of compute, storage and now *network*
- OVS is a key component
- OVS data plane expressed using P4
- P4 to VHDL to automate FPGA programming



# P4 pipeline



# Goal for NFV deployments

